

### **Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

### **Listing of Claims:**

1. (Currently Amended) A plurality of integrated circuits, each of the integrated circuits comprising a processor and ~~non-volatile~~flash memory ~~and a tamper detection line arranged to pass directly over each bit of the non-volatile memory so as to obscure operation of the non-volatile memory~~, and including code for running identical software processes, wherein each of the integrated circuits also includes secret information used by the software process, the secret information in each chip being located in a different location in the flash memory relative to locations in which the same secret information is stored in the flash memory of a plurality of the other chips.
2. (Original) A plurality of integrated circuits according to claim 1, wherein the code on each integrated circuit is such that the software process of each chip knows the location in memory via which the secret information is accessible.
3. (Original) A method of manufacturing a plurality of the integrated circuits of claim 2, including the steps of:
  - manufacturing a plurality of physical integrated circuits; and
  - injecting, into the ~~non-volatile~~flash memory of each of the integrated circuits:
    - code for running a software process; and
    - the same secret information;
  - wherein the secret information is positioned in relatively different locations of the ~~non-volatile~~flash memories of the integrated circuits and the code on each integrated circuit is such that the software process of each integrated circuit knows the location in memory via which the secret information is accessible on that integrated circuit.